

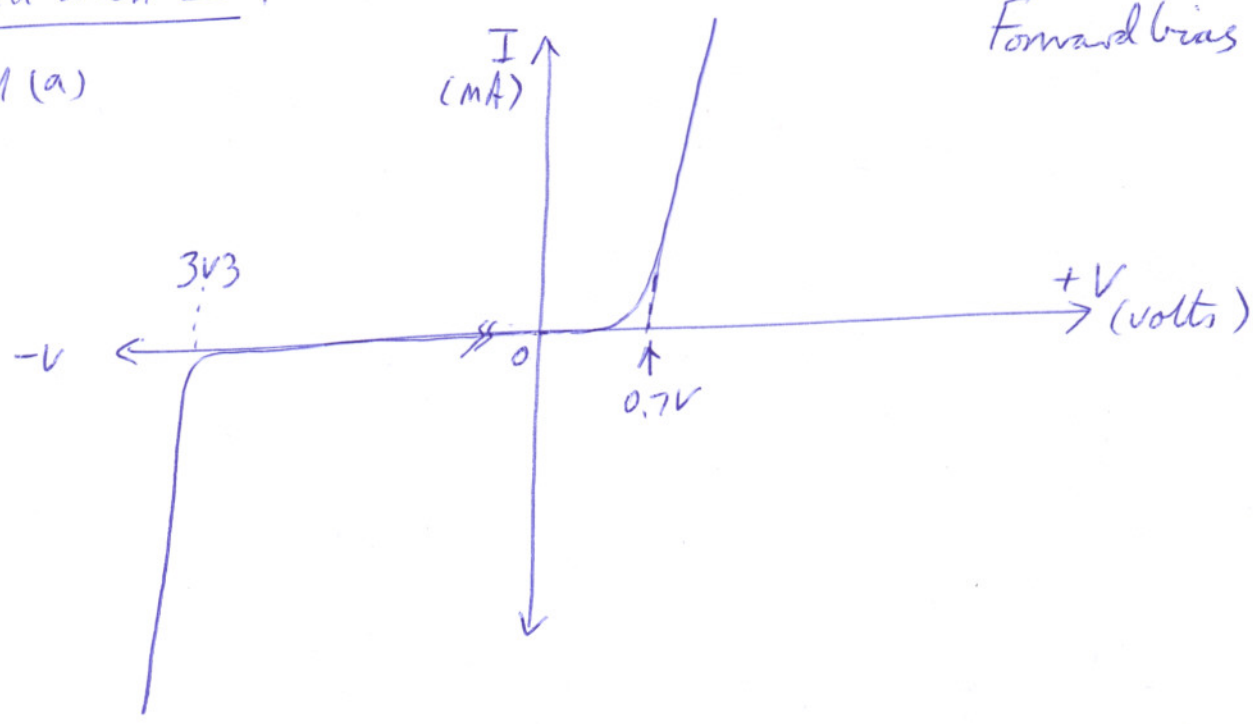
(fsk224T1.2013memo.pdf)

Question 1

- 1.1. D
- 1.2. D
- 1.3. B
- 1.4. A
- 1.5. C
- 1.6. D
- 1.7. B
- 1.8. C
- 1.9. A
- 1.10. D
- 1.11. C
- 1.12. D

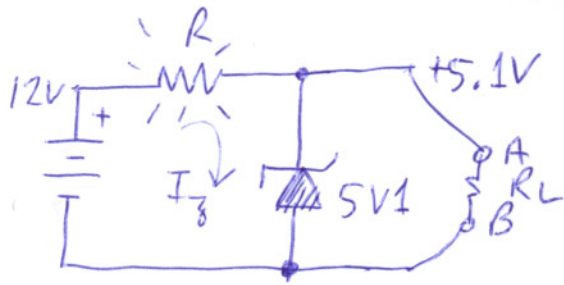
Question 2

2.1 (a)



(b) Possible solutions (there are several, some better than others). 2.

Soln. (i)



$$P_Z = \frac{P_{Zmax}}{2} \text{ (derate)}$$

$$= \frac{1}{2} = 0.5 \text{ W}$$

$$= I_Z V_Z$$

$$\Rightarrow I_Z = \frac{P_Z}{V_Z} = \frac{0.5 \text{ W}}{5.1 \text{ V}} = 0.09803 \text{ A}$$

$$= 98.03 \text{ mA}$$

$$= I_R$$

$$\therefore R = \frac{V_R}{I_R} = \frac{12 - 5.1 \text{ (V)}}{0.09803 \text{ (A)}}$$

$$= 70.4 \Omega \quad (68 \Omega \text{ standard})$$

$$P_R = I_R V_R = 0.09803 (6.9) \text{ W}$$

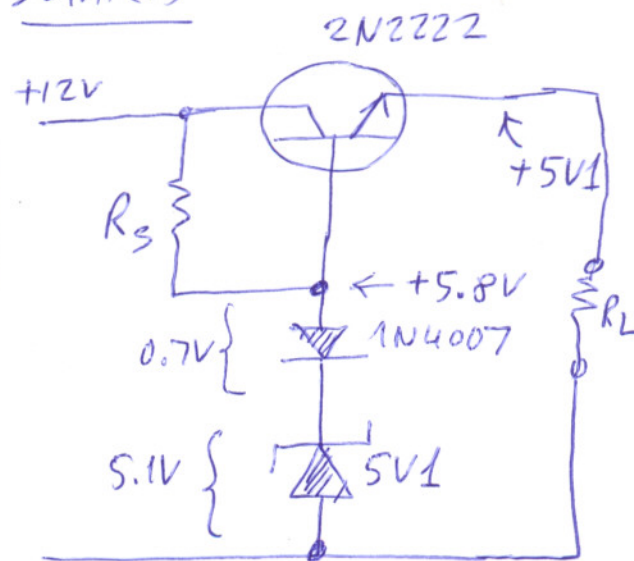
$$= 0.676 \text{ W}$$

Use a resistor of 68Ω rated at higher than 0.676 W e.g. 1 Watt .

Main disadvantage of this circuit:

The load impacts the regulation of the output voltage. The lower the load, the greater the drop in the output voltage.

Soln. (ii)



Using similar reasoning,

$$R_S \geq 68 \Omega.$$

This circuit has the advantage that I_Z can be kept much lower than 98 mA since the transistor base current can be smaller.

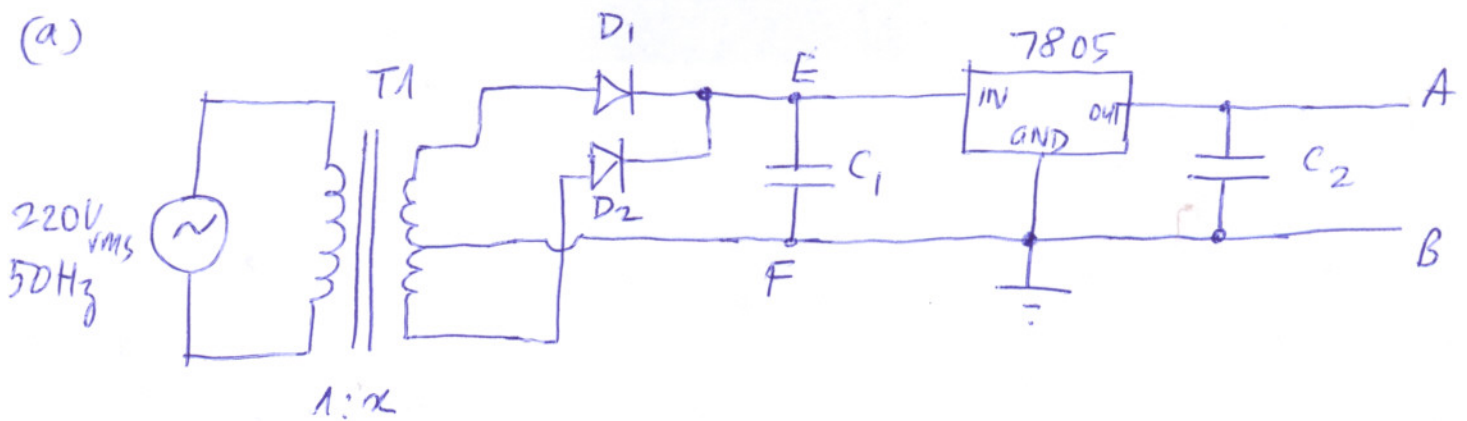
$$I_E = (\beta + 1) I_B = I_L$$

where β = transistor forward current gain.

Q2.2

3.

(a)



Mains

T1: Step down transformer i.e.

$$V_s = \frac{220}{x} (V_{rms})$$

D1, D2: Rectifier diodes in half-wave configuration. Each diode handles 180° of the input. The effective voltage at (E) is $(V_{s,pk} - V_D)$

C1: is a ripple suppression capacitor i.e. "smoothing" filter.

7805: is a three-terminal voltage regulator that produces +5V across A, B. The load is connected across A \rightarrow B

C2: is a high-frequency stabilization capacitor. It prevents the internal circuits of the 7805 from oscillating under certain conditions of loading.

Overall, the circuit produces a regulated, positive 5V (across AB) from the mains supply.

(b) Across C_1 i.e. point (E),

$$V_E = 8V = (V_{S,PK} - V_D)$$

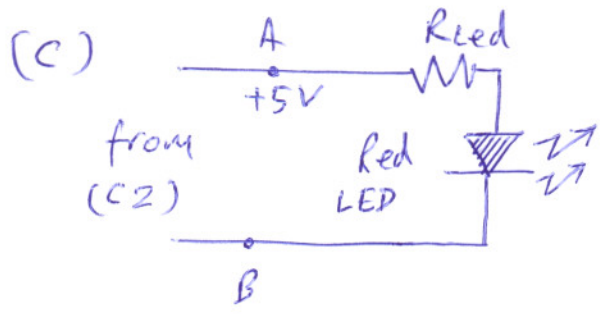
$$\therefore V_{S,PK} = 8 + V_D = 8.7V$$

$$\Rightarrow V_{S,rms} = \frac{V_{S,PK}}{\sqrt{2}} = \frac{8.7}{\sqrt{2}} V = 6.15V$$

$$= \frac{V_{p,rms}}{\alpha}$$

$$= \frac{220}{\alpha}$$

$$\Rightarrow \alpha = \frac{220}{6.15} = 35.77 \approx \underline{\underline{35 \text{ or } 36}}$$



$$V_{Led} = 2V$$

$$I_{Led} \approx 10mA$$

$$\Rightarrow R_{led} = \frac{V_{AB} - V_{Led}}{I_{Led}} = \frac{5 - 2}{10} \frac{(V)}{(mA)}$$

$$= 0.3K = 300\Omega$$

Use a 330Ω resistor, standard.

Q2.4

$$I_B = \frac{V_{CC} - V_{BE}}{R_B} = \frac{12 - 0.7}{240} = \frac{12 - 0.7}{240} \frac{(V)}{(K)} = 0.04708 mA$$

$$= \underline{\underline{47.08 \mu A}}$$

$$I_C = \beta I_B = 50 \times 0.04708 mA = 2.354 mA$$

$$V_{CE} = V_{CC} - I_C R_C = V_{CC} - I_C R_1 = 12 - 2.354(2.2) = 6.8212V$$

$$\approx +6.82V$$

Transistor characteristics

Load-line equation: $V_{CE} = V_{CC} - I_C R_C$

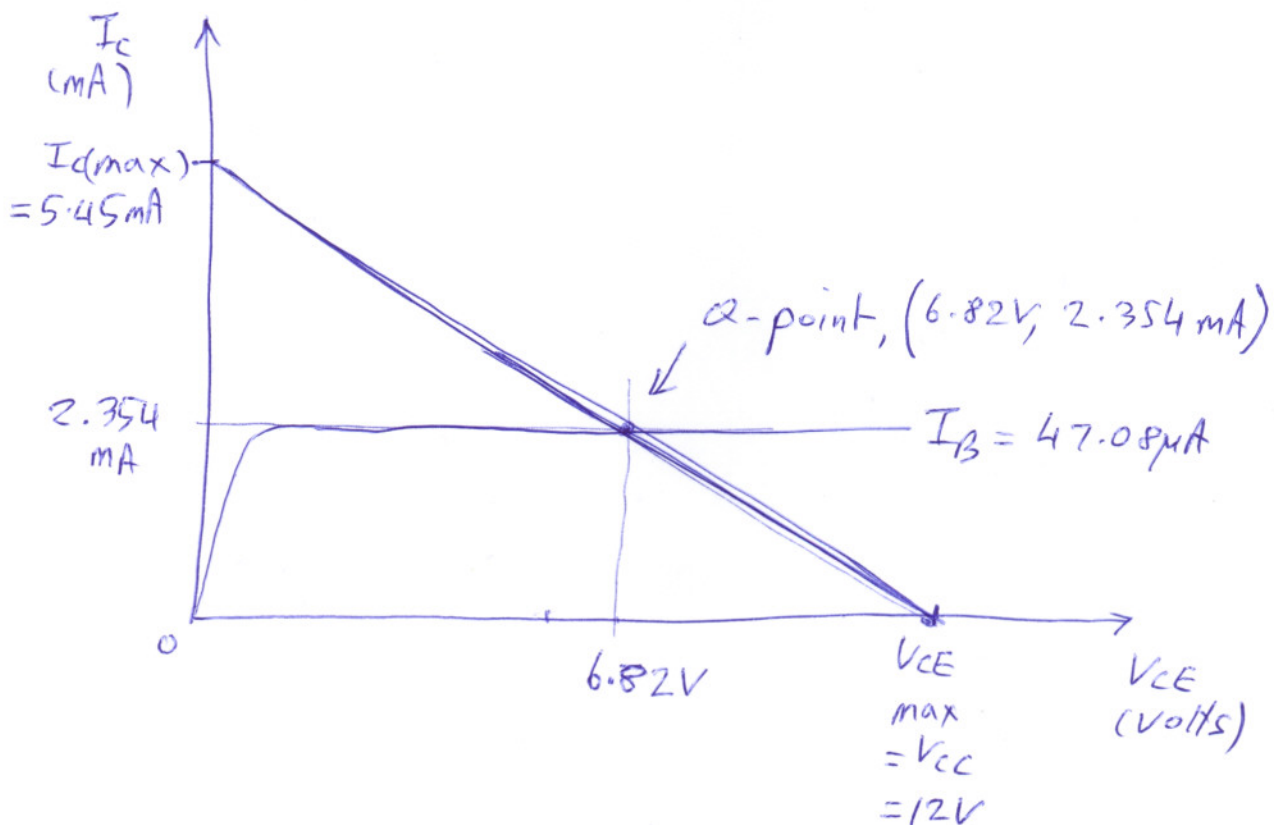
When $I_C = 0$, $V_{CE} = V_{CC}$

When $V_{CE} = 0$, $I_C = \frac{V_{CC}}{R_C} = \frac{12}{2.2} = \frac{12}{2.2} \text{ mA}$
 $= 5.45 \text{ mA}$

When $I_C = 2.354 \text{ mA}$, $V_{CE} = V_{CEQ} = 6.82 \text{ V}$, for

$I_B = 47.08 \mu\text{A}$.

With these values, the characteristic is as shown below —



End of memo